

Typical Features

- ◆ Fixed input voltage, isolated & unregulated, output 2W
- ◆ Efficiency up to 86% (Typ.)
- ◆ Mini size SIP package
- ◆ Isolation voltage 1500VDC
- ◆ Continuous short circuit, self-recovery
- ◆ Operating temperature from -40℃ to +105℃
- ◆ Plastic case, flame class UL94-V0



EN62368-1

Application Field

This series of products can be widely used in the fields of instrument, communication, pure digital circuit, general low frequency analog circuit, relay drive circuit, data exchange circuit, etc.

Typical Product List

Certificate	Part No.	Input Voltage Range		Output Voltage/Current (Vo/Io)		Input Current (mA) Typ. @nominal volt.		Max Capacitive Load	Efficiency (%) @full load nominal volt.	
		Nominal (VDC)	Range (VDC)	Vo (VDC)	Io(mA) Max/Min	Full Load	No Load	uF	Min	Typ.
-	NN2-05S05ANR3	5	4.5	5	400/40	476	6	2400	76	80
-	NN2-05S12ANR3		-	12	167/17	455	18	560	76	80
-	NN2-05S15ANR3		5.5	15	133/13	470	20	560	76	80
CE	NN2-12S3V3ANR3	12	10.8 - 13.2	3.3	400/40	195	10	2400	79	82
CE	NN2-12S05ANR3			5	400/40	195	10	2400	81	84
CE	NN2-12S12ANR3			12	167/17	190	10	560	83	86
CE	NN2-12S15ANR3			15	133/13	192	10	560	80	83
-	NN2-24S05ANR3	24	21.6 - 26.4	5	400/40	98	8	2400	79	82
-	NN2-24S5V5ANR3			5.5	364/36	96	8	2400	80	83
-	NN2-24S7V2ANR3			7.2	278/28	96	8	1000	83	86
-	NN2-24S12ANR3			12	167/17	95	8	560	83	86
-	NN2-24S15ANR3			15	133/13	98	8	560	80	83

Note 1: The maximum capacitive load is the capacitance allowed to be used when the power supply starts up at full load. The converter may not start if the capacitor exceeds this value.

Note 2: The efficiency is tested at the nominal input voltage and the rated load.

Note 3: Please contact Aipu sales for other output voltages requirements of this series but not listed in this table.

Input Specifications

Item	Operating Condition	Min.	Typ.	Max.	Unit
Input inrush voltage (1Sec max.)	5Vdc Input	-0.7	-	9	VDC
	12Vdc Input	-0.7	-	18	
	24Vdc Input	-0.7	-	30	

Input filter	Capacitor Filter
Hot plug	Unavailable

Output Specifications

Item	Operating Condition	Min.	Typ.	Max.	Unit
Output power		0.2	--	2	W
Output voltage accuracy	Please refer to the Output Voltage Deviation Graph (Figure 1)				
Load regulation	10% - 100% load	3.3V output	--	15	%
		Others	--	10	
Line voltage regulation	Input voltage change $\pm 1\%$	3.3V output	--	--	%
		Others	--	--	
Temperature drift coefficient	--	--	--	± 0.03	%/ $^{\circ}\text{C}$
Ripple & Noise	0%-100% load, 20MHz bandwidth	--	75	150	mVp-p
Short circuit protection	Continuous, self-recovery				

Note: The Ripple & Noise is tested by the Twisted Pair Method, please refer to the following test instruction.

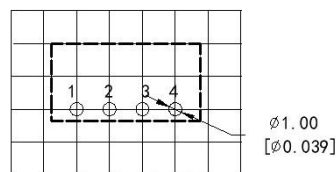
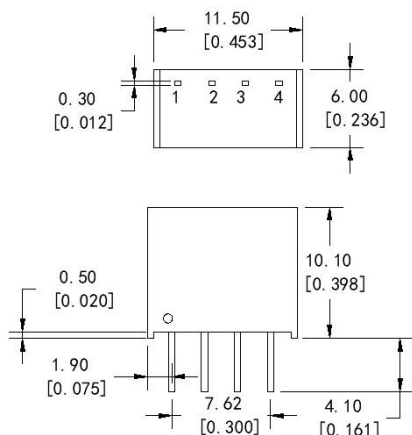
General Specifications

Item	Operating Condition	Min.	Typ.	Max.	Unit
Switching frequency	Nominal input voltage, full load	--	260	--	KHz
Operating temperature	Refer to the Temperature Derating Graph (Figure 2)	-40	--	105	$^{\circ}\text{C}$
Storage temperature		-55	--	+125	$^{\circ}\text{C}$
Case temperature rise	Within the operation derating range	--	30 $^{\circ}$	--	$^{\circ}\text{C}$
Pin soldering temperature	1.5mm from the case, soldering time 10S	--	--	300	$^{\circ}\text{C}$
Relative humidity	No condensing	5	--	95	%RH
Isolation voltage	I/P-O/P, test 1 minute, leakage current $< 1\text{mA}$	1500	--	--	VDC
Insulation resistance	I/P-O/P, @ 500VDC	1000	--	--	M Ω
Isolation capacitance	I/P-O/P, 100KHz/0.1V	--	20	--	pF
MTBF	MIL-HDBK-217F@25 $^{\circ}\text{C}$	3500	--	--	K hours
Vibration	10-150Hz, 5G, 30 Min. along X, Y and Z				
Case material	Plastic in Black, flame class UL94-V0				
Unit weight	1.4g (Typ.)				
Cooling method	Natural air				
Packing	Tube size (525x18x10mm)	43PCS/Tube			
	Carton size (542x110x155mm)	3440PCS/Carton (Total 80 Tubes)			
Unit dimensions	L x W x H	11.50 \times 6.00 \times 10.10 mm		0.453 \times 0.236 \times 0.398 inch	

EMC Performance

Item	Standard	Performance/Class
EMI	CE	CISPR32/EN55032
	RE	CISPR32/EN55032
EMS	ESD	IEC/EN61000-4-2

Mechanical Dimensions



PCB layout vertical view
Grid 2.54x2.54[0.10x0.10]

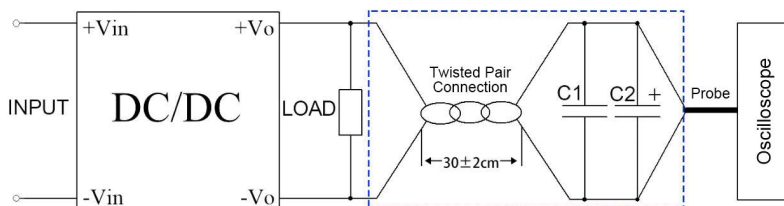
Unit: mm[inch]
Pin section tolerance: $\pm 0.10[\pm 0.004]$
General tolerance: $\pm 0.50[\pm 0.020]$

Pin-out Function Description

Pin No.	1	2	3	4			
Single (S)	GND	+Vin	-Vo	+Vo			

Note: Please take the pin definition on the product label as the right one if it is different than the data sheet description.

Ripple & Noise Test Instruction (Twisted Pair Method, 20MHZ bandwidth)



1. The Ripple & noise test needs 12# twisted pair cables, an oscilloscope which should be set at the Sample Mode, bandwidth 20MHz. 100M bandwidth probe with cap and ground removed. C1(0.1uF) polypropylene capacitor and C2(10uF) high-frequency low-resistance electrolytic capacitor are connected in parallel with the probes and one side of the twisted pair.
2. The power supply output connects to the load by the cables. The other side of the twisted pair (length 30cm \pm 2 cm) should be connected in parallel with the load, the polarity of the output and the oscilloscope probe should not be reversed. The test can start after input power on.

Product Characteristics Graphs

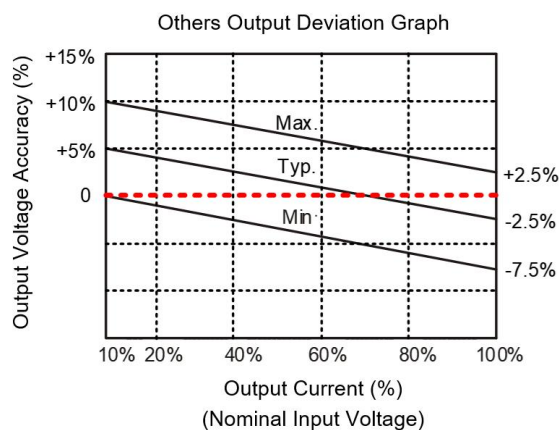
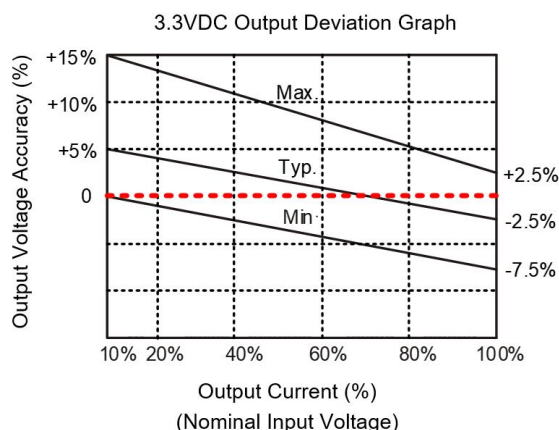


Figure 1

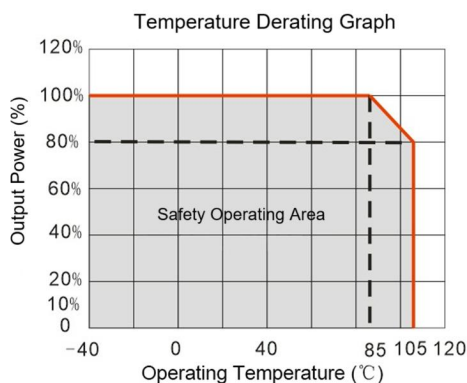


Figure 2

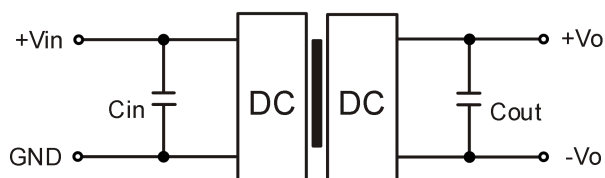
Recommended Circuits for Application

1. Requirement for the output load

- To ensure the converter operating efficiently and reliably, its minimum load should not be less than 10% of the rated load. It is recommended to connect a resistor in parallel to the output when the real load is less than 10% (the sum of the power consumed should be bigger than or equal to 10% of the rated power).
- The maximum capacitive load is tested at full load. The converter may not start or be damaged at the capacitive over-load.

2. Typical application circuits

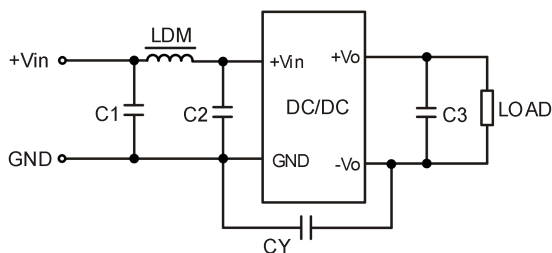
To ensure effectively decrease the input and output ripple and noise, a capacitor filter can be connected at the input and output, the application circuit is shown in the figure below. The suitable filter capacitors should be chosen as the recommended capacitive load values in Table 1. The converter could not start if the capacitance is too big.



Recommended Capacitive Load Values (Table 1)

Vin (Vdc)	Cin	Vout (Vdc)	Cout
5	4.7uF/16V	3.3	10uF/16V
12	2.2uF/25V	5 & 5.5	10uF/16V
24	1uF/50V	7.2	4.7uF/16V
-	-	12	2.2uF/25V
-	-	15	1uF/25V

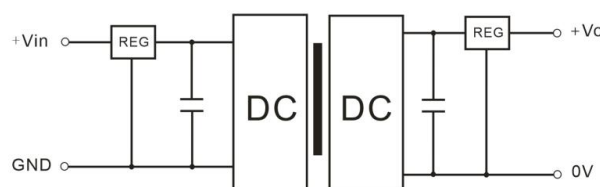
3. Recommended EMC circuit diagram



Input voltage		5Vdc	12/24Vdc
EMI	C1/C2	4.7uF/16V	4.7uF/50V
	CY	270pF/2KVdc	270pF/2KVdc
	C3	Refer to Cout value in Table 1	
	LDM	6.8uH	6.8uH

4. Output voltage regulation and overvoltage protection

The simple solution to achieve the output regulated voltage, over voltage and over current protections is to connect a linear regulator with overheat protection at input or output, and a capacitor filter connected in parallel as below circuits diagrams. Filter capacitive value recommended see table 1, Linear regulator should be chosen according to the actual voltage & current for operating. Or Aipu NW series products are recommended instead.



Application Notice

1. This series of products cannot be connected in parallel to increase the output power, and do not support hot-plug.
2. The product should be used according to the specifications, otherwise it could be permanently damaged.
3. The product performance cannot be guaranteed if it works at a lower load than the minimum load defined.
4. The product performance cannot be guaranteed if it works under the over-load condition.
5. Unless otherwise specified, all values or indicators on this datasheet are tested at $T_a=25^{\circ}\text{C}$, humidity<75%RH, nominal input voltage and rated load (pure resistance load).
6. All values or indicators on this datasheet have been tested based on Aipupower test specifications.
7. The specifications are specially for the parts listed on this datasheet, any other non-standard model performances could be out of the specifications. Please contact our technician for specific requirements.
8. Aipupower can provide customization service.

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